Form PTO-1449				Atty Docket No.				Serial No.			
100				P-9197 10/611,				/611,44	3		
POILE	O PEINFORMATION DISCLOSURE CITATION					Applicant(s)					
ner 0 1 2003	3	IN AN APPLICATION				Swee Yew Choe					
17:	1 mm 10				Filing Date				Group 09//		
,	(Use several sheets if necessary)				June 30, 2003				2816		
PRADEMA			v.s.	PATENT D	OCUMENTS						
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME		CLASS	SUBCLASS		PILING DATE IF APPROPRIATE		
1	AA	4,247,791	01/27/81	Rovell		307	238				
m	AB	5,859,548	01/12/99	Kong		326	113				
M	AD	6,028,454	02/22/00	Elmasry et al.		326	115				
7/0	AC	6,211,704 B1	04/03/01	Kong		326	121				
	AE							\top			
	AF										
	AG							\neg			
	АН							\top			
	AI										
	AJ			1							
	AK							\dashv		_	
	· 4		FOREIG	N PATENT	DOCUMENTS		L	1_			
								T	Transl	ation	
		DOCUMENT NUMBER	DATE	COUNTRY		CLASS	SUBCLAS	s	YES	мо	
	AL										
	AM										
	AN										
	AO										
	AP							\dashv			
	-	OTHER DOCUMENT	S (Including	Author, T	itle, Date,	Pertinent Pa	ges, Etc	.)			
7	AR Choe et al., "Dynamic Half Rail Differential Logic for Low Power", IEEE 1997, pages 1936 to 1939										
M	AS	Jung et al., "Modular Charge Recycling Pass Transistor Logic (MCRPL)", Electronics Letters, 2nd March 2000 Vol. 36 No.5, March 2, 2000, Pages 404 to 405									
M	AT Kong et al., "Charge Recycling Differential Logic for Low-Power Application", ISSC96 secession 18, IEEE 0-780331962/98, 1998, Pages 302 to 448										
Examiner	Z /	War	Date Consid	lered	2/2	Tool H					
EXAMINER:	Initi	al if reference con if not in conforma	sidered, whet	ther or not	citation is	in conforma	nce with	MPE	P 609; dr	aw line	

to applicant(s).

Form PTO-1449					Atty Docket No.			Serial No.			
					P-9197		10/611,4	10/611,443			
O STATE OF THE PROPERTY OF THE					Applicant(s)						
in an application					Swee Yew Choe						
DEC 0 1 2003 5					Filing Date Group						
(Use several sheets if necessary)					June 30, 2003 28/6						
TADEMANDE U.S. PATENT DOCUMENTS											
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME		CLASS	SUBCLAS		FILING DATE IF APPROPRIATE		
	AA										
	AB										
	AC										
	AD				_						
	AE										
	AF										
	AG			 							
	НА				······································	 					
	AI										
	AJ				·-··						
	AK						 				
		······································	PORRIG	N DATENT	DOCUMENTS	<u> </u>	I				
						<u> </u>		Trans	lation		
		DOCUMENT NUMBER	DATE	COUNTRY		CLASS	SUBCLAS		NO		
	AL										
	AM										
	AN										
_	AO										
	AP	_				T	1	1	1		
			.1 1			}	1		1 1		
		OTHER DOCUMENT	IS (Including	Author, T.	itle, Date,	Pertinent Pa	iges, Etc.	.)			
76	AR	OTHER DOCUMENT Choe et al., 25/Processors 420 to 421, 33	"Half Rail and Logic/	Different Paper SP	ial Logic	", ISSCC9	7/Seces	sion	pages		
7L	AR AS	Choe et al., 25/Processors	"Half Rail and Logic/ 36 to 337 a	Different Paper SP nd 489	ial Logic 25.6 IEEE	e", ISSCC9 E 0-7803-3	7/Seces: 721-2/9°	sion 7, 1997,			
N N		Choe et al., 25/Processors 420 to 421, 33	"Half Rail and Logic/ 36 to 337 a Modified Ha IEEE 0-780 "Charge Rec IEEE Journ	Different Paper SP nd 489 If Rail I 3-4455-3/ ycling Di al of Sol	cial Logic 25.6 IEEE Differenti 98, 1998, Afferentia	e", ISSCC9 E 0-7803-3 ial Logic , pages IT	7/Seces: 721-2/9' for Red: -157 to	sion 7, 1997, uced Inte II-160	rnal		
U Examiner	AS	Choe et al., 25/Processors 420 to 421, 33 Won et al., "! Logic Swing", Kong et al., Application", September 1996	"Half Rail and Logic/ 36 to 337 a Modified Ha IEEE 0-780 "Charge Rec IEEE Journ	Different Paper SP nd 489 If Rail I 3-4455-3/ yeling Di al of Sol 67 to 127	cial Logic 25.6 IEEE Differenti 98, 1998, Afferentia	e", ISSCC9 E 0-7803-3 ial Logic , pages IT	7/Seces: 721-2/9' for Red: -157 to	sion 7, 1997, uced Inte II-160	rnal		